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Switching circuit theory is the mathematical study of the properties of networks of idealized switches. Such networks may be strictly combinational logic, in which their output state is only a function of the present state of their inputs; or may also

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contain sequential elements, where the present state depends on the present state and past states; in that sense, sequential circuits are said to include "memory" of past states.

Switching circuit theory - Wikipedia

Engr. Rafael Maramba Room: N303 Day: Monday, Wednesday, and Friday Time: 9:00am - 10:30am

COE117 - Logic Circuits and Switching Theory - Mapúa Files

This course will discuss the basic background of switching circuits, and discuss techniques for mapping the theory to actual hardware circuits. Synthesis and minimization techniques of combinational and sequential circuits shall be discussed in detail. Designing circuits using high-level functional blocks shall also be discussed.

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Switching Circuits and Logic Design - Course

The roots of logic synthesis can be traced to the treatment of logic by George Boole (1815 to 1864), in what is now termed Boolean algebra. In 1938, Claude Shannon showed that the two-valued Boolean algebra can describe the operation of switching circuits. In the early days, logic design involved manipulating the truth table representations as Karnaugh maps. The Karnaugh map-based minimization of logic is guided by a set of rules on how entries in the maps can be combined.

Logic synthesis - Wikipedia

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Logic gates are primarily implemented electronically using

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diodes or transistors, but can also be constructed using electromagnetic relays, fluidics, optics, molecules, or even mechanical elements. In electronic logic, a logic level is represented by a voltage or current, (which depends on the type of electronic logic in use).

LOGIC CIRCUIT AND SWITCHING THEORY

Logic Families Diode, BJT & MOS as a switching element concept of transfer characteristics, Input characteristics and output characteristics of logic gates, Fan-in, Fan-out, Noise margin, circuit concept and comparison of various logic families: TTL, ILL, ECL, NMOS, CMOS Tri-state logic, open collector output, Interfacing between logic families, packing density, power consumption & gate delay.

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Switching Theory: Insight through Predicate Logic Author: Dr. Shimon P. Vingro Published by Springer Berlin Heidelberg ISBN: 978-3-642-07318-2 DOI: 10.1007/978-3-662-10174-2 Table of Contents: Outline and Basics Switching Devices Functions Logic Functions and Gates Synthesis and Duality Karnaugh Maps Utilising Karnaugh Maps Tautologies

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