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Analog Integrated Circuit Design 2nd

I design analog/mixed signal circuits for a living. My colleagues and I have referred to the first edition of this book for 15 years now. We were all a bunch of excited nerds to learn a 2nd edition was coming out, and we weren't disappointed.

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Analog Integrated Circuit Design, 2nd Edition. Tony Chan Carusone, David Johns, Kenneth Martin. ISBN: 978-0-470-77010-8 December 2011 816 Pages. E-Book.

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Overview The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade. Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations.

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This design eliminates any input offset voltage from both the first and second stages by using capacitive coupling. It also has common-mode feedback circuitry for the first pre-amp stage. Here the CMFB is not critical as in fully-differential OpAmps, as we only need to resolve the sign of the signal and nonlinearity is not a major issue.

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Using the drop-down menus above, you'll find spice model files and example netlists supporting material from the book. Also provided are some ideas of suitable lab projects, and a list of known errors.

For Students | Analog Integrated Circuit Design

Electronic circuit design. Summary "The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade. Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations. CMOS devices and circuits have more influence in this edition as well as a reduced amount of text on BiCMOS and bipolar information.

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6.8 Common-Mode Feedback Circuits. 6.9 Summary of Key Points. 6.10 References. 6.11 Problems. Chapter 7: Biasing, References, and Regulators 7.1 Analog Integrated Circuit Biasing. 7.2 Establishing Constant Transconductance. 7.3 Establishing Constant Voltages and Currents

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Yes - your interpretation is consistent with equation (1.121). This definition is particularly suitable for CMOS digital logic circuits where the MOSFETs act as switches that are OFF when $V_{GS} = 0$. In analog circuits, sometimes the source (and even the body terminal) may not be at ground.

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