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## **Am335x Pru Icsc Reference Guide**

ListofFigures. 1  
BlockDiagram..... 14 2  
PRU-ICSSIntegration

## **AM335x PRU-ICSS Reference Guide (Rev. A)**

PRU-ICSS UART serves as an example for PRU firmware-based UART

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## Am335x Pru IcSS

### Reference Guide

peripheral support. The UART firmware (UART FW) allows additional UART instances for SOCs beyond those supported by SOC hardware. The Processor SDK package includes full source code for UART FW.

## **7. PRU-ICSS Firmware — Processor SDK RTOS Documentation**

The Programmable  
Realtime Unit-

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## Am335x Pru Icscs Reference Guide

Industrial

Communications Sub-System (PRU-ICSS) is a versatile component of the AM335x SoC that enables real-time, deterministic Input and Output, even when running a non-realtime operating system. This reference design provides a concrete use case and implementation of the PRU-ICSS to control a Fujitsu® thermal printer. Contained are

# Read Online Am335x Pru IcSS Reference Guide C code examples for the ARM™ to

## **Thermal Printing With the PRU-ICSS on the BeagleBone Black ...**

AM335x PRU-ICSS  
Reference Guide The  
documentation on the  
subsystem is here. TI  
does not support this  
subsystem and all ques  
tions/inquires/problems  
should be directed to  
the community.

Example for the first

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PRU version (Ti  
AM18XX and other  
DSP) [http://www.ti.com  
/tool/sprc940](http://www.ti.com/tool/sprc940)

## **Ti AM33XX PRUSSv2 - eLinux.org**

The description of the  
HIEISR register  
(AM335x PRU-ICSS  
Reference Guide  
chapter 6.4.9) is a bit  
unclear for me: The  
Host Interrupt Enable  
Indexed Set Register  
allows enabling a host  
interrupt output. The



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host interrupt. to  
enable is the index  
value written.

## **PRUSSV2 timer interrupt handling example for AM335x**

PRU-ICSS / PRU\_ICSSG  
Getting Started Guide  
on TI-RTOS 2.2

PRU\_ICSS Dual EMAC  
and Switch The PRU-  
ICSS firmware of a dual  
EMAC and switch is a  
part of the ICSS\_EMAC  
lower level driver. The  
dual EMAC firmware

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realizes a single port Ethernet MAC, which can be used independently on two PRUs

### **PRU-ICSS Getting Started Guide on TI-RTOS (Rev. A)**

Refer to the AM335x PRU ICSS Reference Guide about these register values. Each register is directly addressable as a memory segment, given by the Local Data

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Memory Map in the Reference Manual. This shows the IEP timer starts at address 0x0002\_E000. The memory offset of the GLOBAL\_CFG register is 0h (so no offset).

### **Beaglebone PRU Coding in C - GitHub Pages**

Simple PID Control  
Reference Design With  
PRU®-ICSS Through  
Web Interface Thermal  
Printing with the PRU-

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ICSS on the  
BeagleBone Black  
Reference Design  
Simple Open Real-Time  
Ethernet (SORTE)  
Device With PRU-ICSS  
Reference Design  
EnDat 2.2 System  
Reference Design.  
Evaluation Hardware .  
can be ordered from  
ti.com. PRU Cape;  
Getting Started Guide

### **PRU-ICSS - Texas Instruments Wiki**

PRU-ICSS Ethernet/IP

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## Am335x Pru IcSS

### Reference Guide

Rev A

package is designed for the Sitara processor family (with PRU-ICSS IP) to enable customers add Ethernet/IP Adapter support to their system. The implementation consists of. Firmware (provided as a binary) Driver. PTP Driver; EtherNet/IP Driver (includes DLR) Application; There are plenty of resources to get started with the ...

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Reference Guide

**3.1. PRU ICSS  
EthernetIP User  
Guide – PRU-ICSS  
Industrial ...**

PRU-ICSS Profibus  
Slave package is  
designed for the Sitara  
processor family (with  
PRU-ICSS IP) to enable  
customers add Profibus  
Slave protocol support  
to their system.

Release 01.00.02

Release Date: June  
2018

**PRU ICSS Profibus**  
*Page 14/27*

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## Am335x Pru Icscs Reference Guide

### **Slave - Texas**

### **Instruments Wiki**

The PRU local memory map only includes memory blocks located inside the PRU-ICSS subsystem and the UART at local address 0x0002\_8000 is the UART within the subsystem (see Figure 2 in the "AM335x PRU-ICSS Reference Guide"). Therefore, the local address 0x0002\_8000 maps to global address

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## Am335x Pru Icscs

### Reference Guide

0x4A30\_0000 (PRU-ICSS base address) + 0x0002\_8000, or 0x4A32\_8000.

Regards, Melissa

### **Very confused about PRU local and global memory map ...**

PRU-ICSS Getting

Starting Guide on Linux

(Rev. A) Dec. 10, 2018:

White paper: Ensuring real-time predictability

(Rev. B) Dec. 04, 2018:

Application note: PRU-

ICSS EtherCAT Slave



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Am335x Pru IcSS  
Reference Guide  
Troubleshooting Guide:  
Nov. 07, 2018:  
Application note: PRU-  
ICSS / PRU\_ICSSG  
Migration Guide: Nov.  
05, 2018: White paper:  
PROFINET on TI's  
Sitara™ processors  
(Rev ...

**AM3358 data sheet,  
product information  
and support | TI.com**

The Programmable  
Real-Time Unit and  
Industrial

Communications  
*Page 17/27*

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## Am335x Pru Icscs Reference Guide

Subsystem (PRU-ICSS / PRU-ICSSG) is a small processor that is tightly integrated with an IO subsystem, offering low-latency control of IO pins on TI's SoC devices including the AM335x, AM437x, AM57x, and AM65x Sitara Processors. The PRU-ICSS / PRU-ICSSG is customer-programmable and can be used to implement simple and flexible control functions,

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peripherals, and state machines that directly access IO pins of the device, as well ...

## **Programmable Real-Time Unit and Industrial ... - TI.com**

----- The AM335x PRU Package includes:

Documentation:  
AM335x PRU-ICSS  
overview slides AM18x  
to AM335x PRU  
software migration  
guide AM335x PRU  
Linux Application

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## Am335x Pru Icscs Reference Guide

Driver documentation

CCS AM335x PRU

Debugger training  
slides

AM335x\_PRU\_ICSS gel  
file for use with CCS

AM335x PRU Debugger

PRU software (pru\_sw):

Utils: PASM (PRU

assembler) binary

PASM ...

**GitHub - beagleboar  
d/am335x\_pru\_packa  
ge**

Information on the PRU  
is inconveniently split

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between the TRM and the AM335x PRU-ICSS Reference Guide. For specifics on the AM3358 chip used in the BeagleBone, see the 253 page datasheet. Texas Instruments' has the PRU wiki with more information. More information on using CCS is here.

### **How to run C programs on the BeagleBone's PRU**

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## Am335x Pru IcSS Reference Guide

### microcontrollers

- PRU-ICSS Migration Guide: AM335x to AM437x This migration guide details the Programmable Real-Time Unit (PRU) subsystem hardware differences and outlines software modifications required for porting PRU firmware and ARMÂ® code to AM437x. | Doc
- Sitara Linux ALSA DSP Microphone Array Voice Recognition

# Read Online Am335x Pru Icss Reference Guide

## **AM3358 datasheet - ARM Cortex-A8 Microprocessor The AM335x ...**

Host to PRU (ARM  
CortexA8 to PRU)

Interrupts Each PRU  
has access to host  
interrupt channels

Host0 and Host1  
through register R31  
bit 30 and bit 31  
respectively.

## **Ti AM33XX PRUSSv2 - SIUE**

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## Am335x Pru IcSS

### Reference Guide

Table 1. Line-by-line for shared.pru0.c; Line Explanation; 7.

PRU\_SRAM is defined here. It will be used later to declare variables in the Shared RAM location of memory. Section 5.5.2 on page 75 of the PRU Optimizing C/C++ Compiler, v2.2, User's Guide gives details of the command. The PRU\_SHARED\_MEM refers to the memory section defined in



# Read Online Am335x Pru Icsc Reference Guide am335x\_pru.cmd on line 26.

## **Building Blocks - Applications**

Getting the books  
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challenging means.  
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abandoned going  
taking into  
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contacts to log on

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categorically easy  
means to specifically  
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